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7. A device according to any one of the preceding claims, characterized in that at least one indicator element is defined for a unique counter element.

8. A device according to any one of claims 1 to 6, characterized in that at least one indicator element is defined for at least two counter elements.

5 9. A device according to any one of the preceding claims, characterized in that said data memory contains at least two indicator elements at non-contiguous locations within said data memory.

10. A device according to any one of the preceding claims, characterized in that said memory area comprises means for disabling the operation of said device when an indicator element has gone to the second state.

11. A device according to claims 9 and 10, characterized in said disabling means disable the operation of said device when the state of one indicator element is different from the state of another identical indicator element.

12. A device according to any one of the preceding claims, characterized in that said large maximum number of event occurrences is greater than about one hundred, and preferably, greater than about one thousand.

Sub
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emp.

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